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Bib Data Sheet

CONFIRMATION NO. 5262

SERIAL NUMBER 09/762,589	FILING DATE 03/30/2001 RULE	CLASS 703	GROUP ART UNIT 2123	ATTORNEY DOCKET NO. 108572
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APPLICANTS

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** CONTINUING DATA *****

This application is a 371 of PCT/GB00/02321 06/15/2000 *Dme*

** FOREIGN APPLICATIONS *****

UNITED KINGDOM 9913915.6 06/15/1999 *Dme*

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** 08/28/2001

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY GBN	SHEETS DRAWING 8	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 6
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>Dme</i> Initials <i>Dme</i>				

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TITLE

Semiconductor device simulation method and simulator

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